

=> d hist

(FILE 'USPAT' ENTERED AT 14:15:49 ON 18 MAY 1998)
L1 8335 S EMULAT##
L2 37 S L1 AND DEBUG####(10A) (FIFO OR QUEUE OR BUFFER#)
L3 579 S L1 AND DEBUG#### AND TRAC##
L4 281 S L3 AND INTERFAC##(10A)BUS##
L5 247 S L4 AND (ICE OR CIRCUIT)
L6 27 S L5 AND (395*500 OR 395*527 OR 395*385)/CCLS
L7 35 S L2 NOT L6
L8 27 S L5 AND L6
L9 2 S L2 AND L6

=> d 16 cit 6 8 9 10 20 21

6. 5,630,102, May 13, 1997, In-circuit-emulation event
management system; Thomas M. Johnson, et al., 395/500 [IMAGE
AVAILABLE]

8. 5,594,890, Jan. 14, 1997, Emulation system for emulating CPU
core, CPU core with provision for emulation and ASIC having the CPU
core; Shinichi Yamaura, et al., 395/500; 364/578; 371/22.5, 22.6
[IMAGE AVAILABLE]

9. 5,564,041, Oct. 8, 1996, Microprocessor for inserting a bus cycle in
an instruction set to output an internal information for an
emulation; Shigezumi Matsui, et al., 395/500; 364/232.3, 232.8,
264.1, 264.2, 264.5, 267, DIG.1; 395/740 [IMAGE AVAILABLE]

10. 5,546,562, Aug. 13, 1996, Method and apparatus to emulate VLSI
circuits within a logic simulator; Chandresh Patel, 395/500; 364/488,
578, DIG.1; 395/183.04, 183.09 [IMAGE AVAILABLE]

20. 5,321,828, Jun. 14, 1994, High speed microcomputer in-circuit
emulator; Michael D. Phillips, et al., 395/500; 364/232.3, 264.3,
DIG.1 [IMAGE AVAILABLE]

21. 5,313,618, May 17, 1994, Shared bus in-circuit emulator
system and method; Martin B. Pawloski, 395/500; 364/DIG.1, DIG.2;
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21. 5,321,828, Jun. 14, 1994, High speed microcomputer in-circuit
emulator; Michael D. Phillips, et al., 395/500; 364/232.3, 264.3,
DIG.1 [IMAGE AVAILABLE]

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L5 247 S L4 AND (ICE OR CIRCUIT)
L6 27 S L5 AND (395*500 OR 395*527 OR 395*385)/CCLS

=> d 16 cit 1-27

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2. 5,701,502, Dec. 23, 1997, Isolating a central processing unit from the operating system controlling said unit and its associated hardware for interaction of the unit with data handling apparatus alien to the operating system; Ernest Dysart Baker, et al., **395/500**, 200.31, 290, 406, 680 [IMAGE AVAILABLE]
3. 5,687,371, Nov. 11, 1997, Selection from a plurality of **bus** operating speeds for a processor **bus interface** during processor reset; Phillip G. Lee, et al., **395/651**, 309, **500**, 568 [IMAGE AVAILABLE]
4. 5,684,721, Nov. 4, 1997, Electronic systems and **emulation** and testing devices, cables, systems and methods; Gary L. Swoboda, et al., **364/578**, 488, 489; **395/500** [IMAGE AVAILABLE]
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7. 5,603,012, Feb. 11, 1997, Start code detector; Martin W. Sotheran, **395/500**; **370/450** [IMAGE AVAILABLE]
8. 5,594,890, Jan. 14, 1997, **Emulation** system for **emulating** CPU core, CPU core with provision for **emulation** and ASIC having the CPU core; Shinichi Yamaura, et al., **395/500**; **364/578**; **371/22.5**, 22.6 [IMAGE AVAILABLE]
9. 5,564,041, Oct. 8, 1996, Microprocessor for inserting a bus cycle in an instruction set to output an internal information for an **emulation**; Shigezumi Matsui, et al., **395/500**; **364/232.3**, 232.8, 264.1, 264.2, 264.5, 267, **DIG.1**; **395/740** [IMAGE AVAILABLE]
10. 5,546,562, Aug. 13, 1996, Method and apparatus to **emulate** VLSI circuits within a logic simulator; Chandresh Patel, **395/500**; **364/488**, 578, **DIG.1**; **395/183.04**, 183.09 [IMAGE AVAILABLE]
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20. 5,321,828, Jun. 14, 1994, High speed microcomputer **in-circuit emulator**; Michael D. Phillips, et al., 395/500; 364/232.3, 264.3, DIG.1 [IMAGE AVAILABLE]

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